

## 7. Specifications

7.1 Measured signal system	7-2
7.2 Phase sensitive detector section	7-4
7.3 Reference signal system	7-5
7.4 Internal oscillator	7-6
7.5 Measurement output section	7-7
7.6 Monitor output (input signal to phase sensitive detector)	7-9
7.7 Auxiliary input (DC voltage measurement)	7-9
7.8 Auxiliary output (DC voltage output)	7-9
7.9 Data memory	7-9
7.10 Automatic setting function	7-10
7.11 External interface (GPIB and RS-232)	7-10
7.12 General specifications	7-11

## 7.1 Measured signal system

### a) Voltage input

Input connector	Two BNC receptacles (for A and B)
Input format	A (single end) and A-B (differential)
Voltage sensitivity	2nV to 1V full scale (1-2-5 sequence)
Voltage accuracy	$\pm 0.5\%$ (1kHz, signal level of 1mV or greater, $23 \pm 5^\circ\text{C}$ ) $\pm 2\%$ (1kHz, signal level of $1\mu\text{V}$ or greater) (Dynamic reserve LOW, 30% or higher of full scale) $\pm 0.5\%$ (for DC coupling, 20kHz or less) $\pm 1\%$ (for DC coupling, 50kHz or less) $\pm 2.5\%$ (for DC coupling, 100kHz or less) (Dynamic reserve at LOW, sensitivity 1V, signal level 1V)
Gain drift	$\pm 100\text{ppm}/^\circ\text{C}$ (typically 1kHz) High dynamic reserve may deteriorate the gain drift.
Input impedance	$10\text{M}\Omega \pm 1.5\%$ , about 50pF in parallel
Input referred noise	$6\text{nV}/\sqrt{\text{Hz}}$ (max), $4.5\text{nV}/\sqrt{\text{Hz}}$ (typically) for dynamic reserve at LOW, sensitivity set to 2mV or less, frequency at 1kHz and input short-circuited.
Maximum allowable input voltage	$\pm 7\text{V}$ (DC coupling) 5Vrms (AC coupling, sine wave); as allowable noise level for sensitivity at 1V and dynamic reserve at HIGH
Maximum nondestructive input voltage	For AC coupling: Sine wave 10Vrms, DC $\pm 50\text{V}$ For DC coupling: $\pm 14\text{V}$
Frequency range	1mHz to 100kHz (DC coupling), 0.5Hz to 100kHz (AC coupling)
Common-mode rejection ratio	120dB (typically 1kHz), 100dB (min. 50Hz to 1kHz) for AC coupling, dynamic reserve at LOW, sensitivity set to 20mV or less, at MEDIUM and 2mV or less
Harmonic distortion	-90dBtyp (1kHz), -80dB or less (10Hz to 5kHz), -70dB or less (5 to 10 kHz) all for dynamic reserve at LOW, sensitivity at 1V, signal level at 1V

**b) Current input**

Input connector	BNC receptacle (I)
Input type	Single-ended
Current sensitivity	50fA to $1\mu\text{A}$ 1-2-5 sequence (for conversion gain $10^6\text{V/A}$ ) 5fA to 10nA 1-2-5 sequence (for conversion gain $10^8\text{V/A}$ )
Current accuracy	$\pm 1\%$ (1kHz and magnitude of signal 1nA to $1\mu\text{A}$ for conversion gain $10^6\text{V/A}$ ; 10Hz and magnitude of signal 10pA to 10nA for conversion gain $10^8\text{V/A}$ both for dynamic reserve at LOW and magnitude of signal at 30% or greater of sensitivity full scale and $23 \pm 5^\circ\text{C}$ )
Gain drift	$\pm 150\text{ppm}/^\circ\text{C}$ typically (at 1kHz for $10^6\text{V/A}$ and 10Hz for $10^8\text{V/A}$ ) High dynamic reserve may deteriorate the gain drift.
Input referred noise	$130\text{fA}/\sqrt{\text{Hz}}$ typically (1kHz for conversion gain $10^6\text{V/A}$ ) $13\text{fA}/\sqrt{\text{Hz}}$ (125Hz for conversion gain $10^6\text{V/A}$ )
Input impedance	$<1\text{k}\Omega$ (500Hz for conversion gain $10^6\text{V/A}$ ) $<20\text{k}\Omega$ (50kHz for conversion gain $10^6\text{V/A}$ , for reference) $<20\text{k}\Omega$ (500Hz for conversion gain $10^8\text{V/A}$ )
Maximum allowable input current	$\pm 7\mu\text{A}$ ; as allowable noise level for conversion gain of $10^6\text{V/A}$ , sensitivity at $1\mu\text{A}$ and dynamic reserve at HIGH
Maximum nondestructive input current	10mA
Frequency range	1mHz to 50kHz (DC coupling, for conversion gain $10^6\text{V/A}$ ) 1mHz to 500Hz (DC coupling, for conversion gain $10^8\text{V/A}$ ) 0.5Hz or greater for both of the above in AC coupling

**c) Specifications common to voltage and current**

Input coupling	AC/DC (AC coupling after conversion to voltage for current input)
Input ground	Float/chassis ground Voltage between chassis and signal ground is $\pm 1\text{V}$ max when floating Ground impedance is about $10\text{k}\Omega$ (DC) when floating Ground impedance is about $10\Omega$ (DC) when chassis is grounded
Line filter	Power supply frequency (50 or 60Hz) and its doubled value Attenuation in center frequency 20dB or greater
Anti-aliasing filter	ON/OFF (OFF; response speed and phase drift to be improved when not in use)

## 7.2 Phase sensitive detector section

Dynamic reserve	100dB or greater; the ratio of sensitivity full scale (rms) to maximum allowable noise level (rms)
Time constant	10 $\mu$ s to 30ks (1-3 sequence)
Attenuation slope	6, 12, 18 and 24dB/oct
Synchronous filter	ON/OFF; when filter is ON, output ripples can be reduced by moving average of integral cycle. However, ripples may not be attenuated sufficiently if 200Hz is exceeded.
Phase noise	0.001 $^{\circ}$ rms typically (1kHz) Sine wave reference signal, time constant 100ms, attenuation slope 18 dB/oct 0.003 $^{\circ}$ rms typically (100kHz) Sine wave reference signal, time constant 100ms, attenuation slope 12 dB/oct This specification may not be applicable if noise or jitter in reference signal is large, or amplitude is less than 1Vrms.
Phase drift	Within $\pm 0.01^{\circ} / ^{\circ}\text{C}$ ( $\geq 100\text{Hz}$ , $\leq 10\text{kHz}$ ) Within $\pm 0.1^{\circ} / ^{\circ}\text{C}$ ( $> 10\text{kHz}$ , $\leq 60\text{kHz}$ ) Within $\pm 0.2^{\circ} / ^{\circ}\text{C}$ ( $> 60\text{kHz}$ ) This specification may not be applicable if an external sine wave signal of less than 1Vrms is used as the reference signal.

## 7.3 Reference signal system

Reference mode	REF IN (external), INT OSC (internal oscillator), SIGNAL (measured signal)
Frequency range	0.5mHz to 102kHz for TTL input or INT OSC 0.5Hz to 102kHz for SINE input or SIGNAL
Harmonics measurement	1 to 19999 times of the reference signal (frequencies of harmonics must be within the above frequency range)
Input type	Single-ended
Input impedance	Approx. $1M\Omega$ (1kHz), 100pF or less in parallel
Input voltage range	0.3 to 30Vp-p (SINE input, sine wave) 0 to 5V (TTL input, threshold voltage of approx. 1.5V)
Maximum nondestructive input voltage	$\pm 40V$
External reference signal waveform	SINE/TTL POS/TTL NEG
SINE :	A regular waveform that crosses the mean value only twice in a cycle Duty factor is 10 to 90% for a square wave The point at which the wave crosses the mean value upward from below is defined as 0 degree.
TTL POS:	A waveform with stable cycle that crosses the threshold voltage only twice in a cycle. The point at which low level changes to high level is defined as 0 degree.
TTL NEG:	A waveform with stable cycle that crosses the threshold voltage only twice in a cycle. The point at which high level changes to low level is defined as 0 degree.
External reference signal synchronization time	2 cycles + 50ms typically (Synchronization time is zero when internal oscillator is used)
Phase adjustment range	$-180.00^\circ$ to $+179.99^\circ$ , resolution $0.01^\circ$
Orthogonality	Within $\pm 0.001^\circ$
Phase accuracy	$\pm 1^\circ$ (DC coupling, $\leq 10kHz$ , sensitivity 1V, signal level 1V) $\pm 5^\circ$ (DC coupling, $\leq 100kHz$ , sensitivity 1V, signal level 1V)
Frequency display resolution	4 1/2 digits (maximum 19999) or 0.1mHz, whichever is greater
Frequency measurement accuracy	$\pm 30ppm$
UNLOCK indication	Indicates that the system is not synchronized to external reference signal.
Reference signal output	Connector BNC receptacle (rear panel) Signal level TTL (0 to 5V)

## 7.4 Internal oscillator

Oscillation frequency	Range	0.5mHz to 105kHz
	Resolution	4 1/2 digits (maximum 19999) or 0.1mHz, whichever is greater
	Accuracy	$\pm 30$ ppm
Output voltage Range	Full scale	0.0500Vrms, 0.500Vrms, 5.00Vrms (no loaded) 3-range manual switching
	Resolution	0.1mV, 1mV, 10mV (for each range)
	Accuracy	2% of set value + 0.5% of full scale (frequency $\leq 1$ kHz)
		5% of set value + 0.5% of full scale (frequency $\leq 10$ kHz)
Stability		20% of set value + 0.5% of full scale (frequency $\leq 100$ kHz) When approx. 102kHz is exceeded, the amplitude suddenly drops.
		$\pm 50$ ppm/ $^{\circ}$ C typically (for 1kHz, 1rms)
Maximum output current		$\pm 10$ mA (when 5Vrms, load impedance $\geq 660 \Omega$ )
Output impedance		$50 \Omega \pm 3\%$ (1kHz)
Harmonic distortion		-80dB or less (for 20Hz to 5kHz, maximum amplitude selected)
		-70dB or less (for frequencies $\leq 100$ kHz, maximum amplitude selected)



**b) Analog output**

## Front panel output

DATA1 OUT Same as DATA1 display parameter

DATA2 OUT Same as DATA2 display parameter

Update rate for the above data

X, Y, R and  $\theta$  : 256k samples/s

Others : 16k samples/s

The rate may be slower depending on the measurement conditions.

## Rear panel output

X OUT Same as X of DATA1 display parameter

Y OUT Same as Y of DATA2 display parameter

Update rate for the above data

16k samples/s. The rate may be slower depending on the measurement.

## Common specifications

Connector BNC receptacle

Maximum output voltage  $\pm 12V$  ( $\theta$ , % is  $\pm 10V$ )Maximum output current  $\pm 6mA$ Output impedance Approx.  $1k\Omega$  (DC)Output voltage accuracy  $\pm$ (Analog output voltage equivalent to 0.35% of displayed value +15mV); (in DC)Measurement values corresponding to meter full scale (analog output  $\pm 10V$ )

X, Y and R Sensitivity set value/EXPAND magnification

NOISE Sensitivity setting

AUX IN1 and AUX IN2  $\pm 10V$  $\theta$   $\pm 180^\circ$ Ratio  $\pm 2$ % indication  $\pm 200\%$ dB indication  $\pm 100dB$ **c) Analog meter**

DATA1 Indicates the same parameter as DATA1 display parameter

DATA2 Indicates the same parameter as DATA2 display parameter.

## 7.6 Monitor output (input signal to phase sensitive detector)

Connector	BNC receptacle
Maximum output voltage	$\pm 12\text{V}$
Maximum output current	$\pm 6\text{mA}$
Output impedance	Approx. $1\text{k}\Omega$ (1kHz)

## 7.7 Auxiliary input (DC voltage measurement)

Number of channels	2
Connectors	Two BNC receptacles (AUX IN1 and AUX IN2 on rear panel)
Maximum allowable input voltage	$\pm 12\text{V}$
Maximum nondestructive input voltage	$\pm 40\text{V}$
Input impedance	Approx. $1\text{M}\Omega$ , 100pF or less in parallel
Accuracy	$\pm (0.35\% \text{ of reading value} + 15\text{mV})$
Frequency band	DC up to approx. 130Hz (-3dB)
Sampling rate	16k samples/s

## 7.8 Auxiliary output (DC voltage output)

Number of channels	2
Connectors	Two BNC receptacle (AUX OUT1 and AUX OUT2 on rear panel)
Setting voltage range	$\pm 10.000\text{V}$ (resolution 0.001V)
Maximum output current	$\pm 5\text{mA}$
Output impedance	Approx. $1\text{k}\Omega$
Output voltage accuracy	$\pm (0.35\% \text{ of setting} + 15\text{mV})$

## 7.9 Data memory

Type of data	Select from the following: DATA1, DATA2, DATA1/DATA2, DATA2/AUX IN2, DATA1/ DATA2 /reference signal frequency, DATA1/DATA2/AUX IN1 /AUX IN2
Data resolution	16 bits (reference signal frequency is 32 bits)
Recording capacity	64K data (total of all parameters to be recorded; assuming 16 bits per data)
Number of memory divisions	1, 2, 4, 8, 16 and 32
Sampling interval	1/16ms, 1/8ms, 1/4ms, 1/2ms, 1ms, 2ms, 5ms, 10ms, 20ms, 50ms, 100ms, 200ms, 500ms, 1s, 2s, 5s, 10s, 20s; or by trigger signal

Trigger signal	TRIG IN on rear panel or via external interface		
TRIG IN	Connector	BNC receptacle	
	Signal level	TTL level (falling edge)	
	Input impedance	Approx. 10k $\Omega$	
	Maximum nondestructive input voltage	$\pm 40V$	
	Minimum trigger interval	1/16ms	
Operation	Via external interface		

## 7.10 Automatic setting function

AUTO SET	Sets optimum sensitivity, dynamic reserve, time constant, phase etc. according to the input signal.
Sensitivity	Adjusts sensitivity of voltage or current, and dynamic reserve according to the input signal.
Time constant	Adjusts time constant corresponding to the frequency of reference signal.
Phase	Sets the phase of reference signal so that the measurement $\theta$ of phase will be zero.
Offset	Sets all offsets so that output of X and Y will be zero.

## 7.11 External interface (GPIB and RS-232)

Either of GPIB and RS-232 is capable of setting and reading parameters, reading statuses and reading measurement data.

In addition, various commands are provided most of which are compatible with NF's 5610B/5600A as much as practicable to ensure previous programs are available with minimal modification.

### a) GPIB

Standard	Conforming to IEEE std 488.1-1987 and IEEE std 488.2-1992
Interface functions	SH1, AH1, T6, L4, SR1, RL1, PPO, DC1, DT1, CO and E1

### b) RS-232

Baud rate	1200, 2400, 4800, 9600 and 19200
Data bit length	7 or 8 bits
Stop bit length	Fixed to 1 for reception and 2 for transmission
Parity	Even, Odd, or None

Please note that RS-232 does not provide the function that corresponds to service request of GPIB.

## 7.12 General specifications

Power output to preamplifier	$\pm 24\text{V}$ , $\pm 50\text{mA}$
Setting memories	9
Initializing function	Returns to specified initial setting
Key lock	ON/OFF
Lamp control	ON/OFF
Fan control	ON/OFF
Power supply voltage range	100/120/230V $\pm 10\%$
Power supply frequency range	50/60Hz $\pm 2\text{Hz}$
Power consumption	50VA maximum
Temperature/humidity range for performance guarantee	0 to $+40^{\circ}\text{C}$ , 10 to 95%RH (no dew condensation) Some of specifications limit the temperature range.
Storage temperature/humidity range	-10 to $+50^{\circ}\text{C}$ , 10 to 85%RH (no dew condensation)
Outer dimensions	434 (W) x 132.5 (H) x 450 (D) mm (excluding protruding portions)
Mass	Approx. 10kg

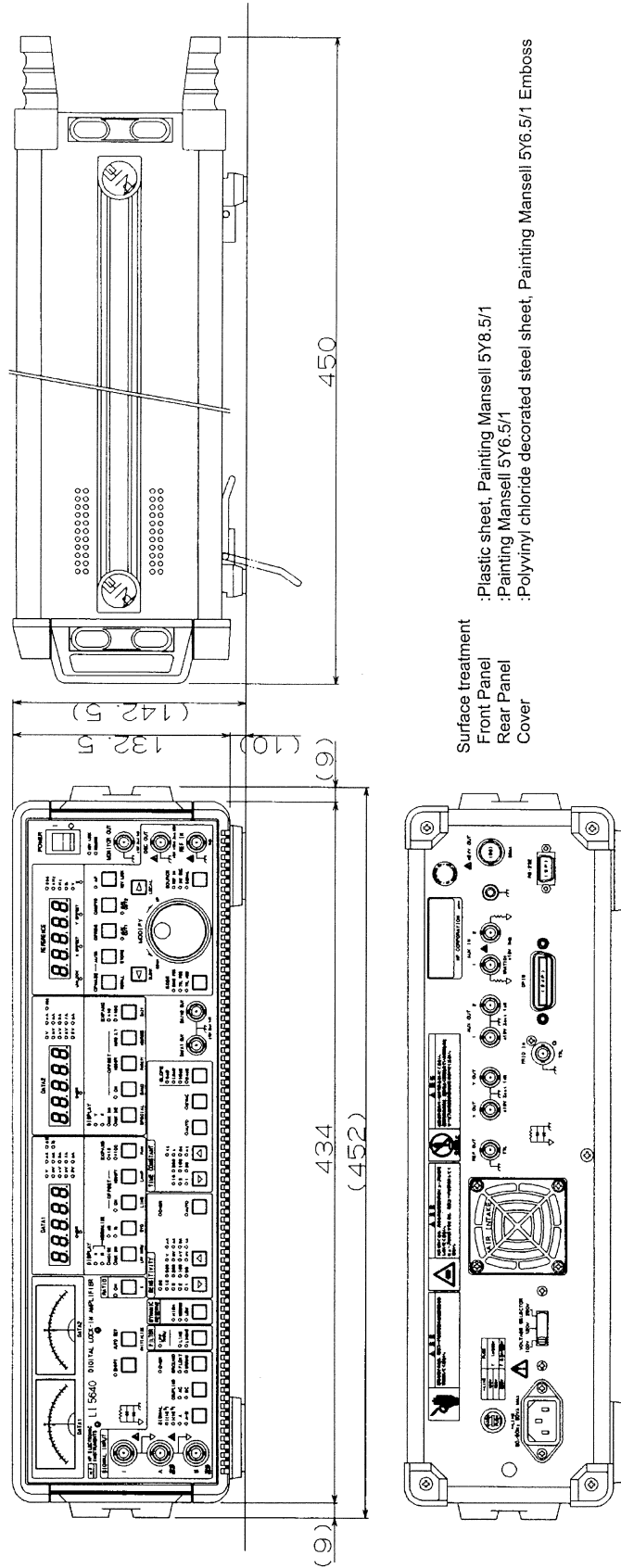


Figure 7-1 External Dimensions

## WARRANTY

**NF CORPORATION** certifies that this instrument was thoroughly tested and inspected and found to meet its published specifications when it was shipped from our factory.

All **NF** products are warranted against defects in materials and workmanship for a period of one year from the date of shipment. During the warranty period of, **NF** will, at its option, either will repair the defective product without any charge for the parts and labor, or either repair or replace products which prove to be defective. For repair service under warranty, the product must be returned to a service center designated by **NF**. Purchaser shall prepay all shipping cost, duties, and taxes for the product to **NF** from another country, and *NF* shall pay shipping charge to return the product to purchaser.

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LI5640 Multi-function Digital Lock-in Amplifier Instruction Manual

**NF CORPORATION**

3-20 Tsunashima Higashi 6-chome, Kohoku-ku. Yokohama-shi  
223-8508, JAPAN

Phone: 81-45-545-8128 Fax: 81-45-545-8187

